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Efficiency of High-speed On-Chip Interconnect: Trade-off and Optimization
Channel-and-Circuits-Aware, Energy-Efficient Coding for High-speed Links
Realization of Baseband DSP Core for the Wireless Gigabit LAN
A 77-GHz Receiver for Millimeter Wave Imaging
Optical-feedback OLED Display Using Integrated Organic Technology
A Wideband ∆Σ Comparator-based Switched-capacitor Circuits (CBSC)
A Time-based Energy-efficient Analog-to-Digital Converter
An Analog Storage Cell with 5 Electrons/sec Leakage
High-performance Time-to-Digital Conversion and Applications
Digital Techniques for the Linearization of RF Transmitters
Advanced Delay-locked Loop Architecture for Chip-to-Chip Communication
Techniques for Highly Digital Implementation of Clock and Data Recovery Circuits
High-speed Continuous-time Sigma-Delta A/D Converters
Digital Implementation and Calibration Technique for High-speed Time-Interleaved Comparator-Based Switch Capacitor ADC
Convex Optimization of Integrated Systems Using Geometric Programming
A Quasi-convex Optimization Approach to Parameterized Model-order Reduction
Development of Specialized Basis Functions and Efficient Substrate Integration Techniques for Electromagnetic Analysis of Interconnect and RF Inductors
A 3-D SRAM Architecture and Circuits
Carbon Nanotube - CMOS Chemical Sensor Integration
A Power-efficient Multi-local PLL Clock Distribution
A 77-GHz Receiver for Millimeter Wave Imaging
Optimization of System and Circuit Parameters in Wideband OFDM Systems
A Wideband ∆Σ Digital-RF Modulator
Area- and Power-Efficient Integrated Transceivers for Gigabit Wireless LAN
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for additional reading...
Characterization of Organic Field-Effect Transistors for OLED Displays