We’re going to treat op-amps in some detail in this class. They are very useful as all-around circuit blocks, as we shall see in the next few lectures. From the standpoint of learning circuit design, though, they are extremely good for sensitizing us to the issues surrounding DC amplification.

Let’s pause and take stock of some of the things that AC coupling has given us.

1. We don’t worry about what our reference level is:

2. DC offsets? What DC offsets?

3. “Bias Picture” vs. “Midband Picture”
But suppose our input has a DC component that we care about?

**CLASS EXERCISE**
Consider the following circuit, for which $V_{BE}$ is 0.6V.

Suppose we have chosen our reference output level to be +2V. That is, when $V_{IN}$ is 0, $V_o$ is 2V.

1. Pick $R_L$ to achieve this.
2. What range is $R_L$ restricted to if we demand accuracy to within ±1mV?

(Workspace)
When we design for DC performance, then, we go to a whole new bag of circuit tricks. Differential structures and op-amps are big parts of that bag…

So what are op-amps useful for? First, recall how to analyze op-amp circuits quickly:

(1) Non-inverting amplifier

Start by making no assumptions about the gain, \( a \), of op-amp.

\[
\begin{align*}
    v_N &= \frac{R_1}{R_1 + R_2} v_0 \\
    v_0 &= a \left( v_p - v_N \right) \\
    v_0 &= a \left( \frac{R_1}{R_1 + R_2} v_p - v_N \right)
\end{align*}
\]

Solve for \( V_0 \):

\[
\left( 1 + a \frac{R_1}{R_1 + R_2} \right) v_0 = a v_i
\]

\[
\frac{v_0}{v_i} = \frac{a}{1 + a \frac{R_1}{R_1 + R_2}}
\]

And of course, if we are lucky enough to have large gain \( a \),

\[
\frac{v_0}{v_i} = 1 + \frac{R_2}{R_1}
\]
Before we leave this, let’s go back and look at

\[ v_0 = a(v_p - v_n) \]

Notice that infinite gain \( a \), together with finite values \( v_0 \), imply \( v_p - v_n \to 0 \). Put (yet) another way:

\[ v_p - v_n = \frac{v_0}{a} = \frac{1}{1 + a \frac{R_1}{R_1 + R_2}} \]

As \( a \) goes to infinity, it must be true that \( v_p = v_N \).

(2) Inverting amplifier and Summer

\[
\begin{align*}
\frac{v_i - 0}{R_1} &= \frac{0 - v_0}{R_2} \\
\Rightarrow \quad \frac{v_0}{v_i} &= -\frac{R_2}{R_1}
\end{align*}
\]

\[
\begin{align*}
\frac{v_i - 0}{R_1} + \frac{v_2 - 0}{R_2} + \frac{v_N - 0}{R_N} &= \frac{0 - v_0}{R_f}
\end{align*}
\]

Allows you to perform weighted sum!
6.301 Solid State Circuits
Recitation 10: Op-Amp Applications
Prof. Joel L. Dawson

\[ v_0 = -R_f \sum_{i=1}^{N} \frac{v_i}{R_i} \]

(3) Differential Amplifier

Those of you who continue on to do advanced circuit design, particularly for ICs, will probably work with differential signal paths. An example is a fully differential amplifier:

\[ v_{IN} = (v_i + v_{i-}) \]
\[ v_{OUT} = (v_0 + v_{0-}) \]

Sometimes it becomes necessary to convert to a single-ended signal, that is

\[ v_0 = v_i + v_{i-} \]

If we’re concerned only with AC signals, we can use a transformer (sometimes called a balun):

But if we care about the DC component, we must find another solution. Once again, the op-amp rides to the rescue:
We can lower our heads and grind on this one, or take advantage of superposition and our previous results:

\[ V_0 = \left( \frac{z_4}{z_3 + z_4} \right) \left( \frac{z_2}{z_1} \right) V_b - \frac{z_2}{z_1} V_a \]

Now suppose we choose \( \frac{z_4}{z_3} = \frac{z_2}{z_1} \)

\[ V_0 = \left( \frac{z_4}{z_3} \right) \left( 1 + \frac{z_2}{z_1} \right) V_b - \frac{z_2}{z_1} V_a \]

\[ V_0 = \frac{z_4}{z_3} V_b - \frac{z_2}{z_1} V_a = \frac{z_2}{z_1} (V_b - V_a) \]

The op-amp has many, many useful applications. Soon, we will learn how they work on the inside.